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U.S. UTILITY Patent Application

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O.I.P.E.	PATENT DATE
SCANNED 4/1 Q.A. TM	

APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER
09/767830	F	~~257~~ 438	~~257~~ 738	~~2811~~ 2812	~~Knudy~~ Knudy

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TITLE
Semiconductor integrated circuit device and process for manufacturing the same

PTO-2040
12/99

ISSUING CLASSIFICATION

ORIGINAL		CROSS REFERENCE(S)	
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)
INTERNATIONAL CLASSIFICATION			
			☐ Continued on Issue Slip Inside File Jacket

☐ TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
☐ The term of this patent subsequent to _____ (date) has been disclaimed.				NOTICE OF ALLOWANCE MAILED	
	_____ (Assistant Examiner)		_____ (Date)		
☐ The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____				ISSUE FEE	
				Amount Due	Date Paid
	_____ (Primary Examiner)		_____ (Date)		
☐ The terminal ____months of this patent have been disclaimed.				ISSUE BATCH NUMBER	
	_____ (Legal Instruments Examiner)		_____ (Date)		

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